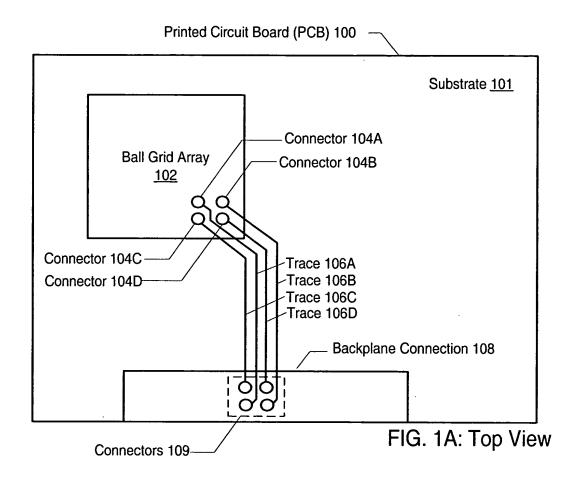
Attorney Docket No.: CIS0203US

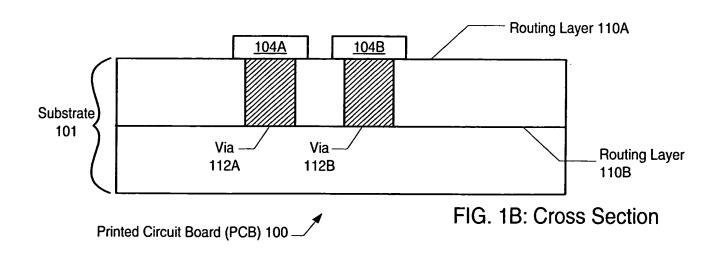
First Named Inventor: Gopakumar Parameswaran

Title: SYSTEM AND METHOD FOR REDUCING CROSSTALK BETWEEN

VIAS IN A PRINTED CIRCUIT BOARD

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VIAS IN A PRINTED CIRCUIT BOARD

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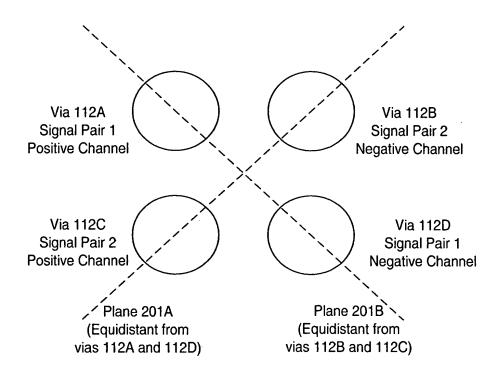


FIG. 2A

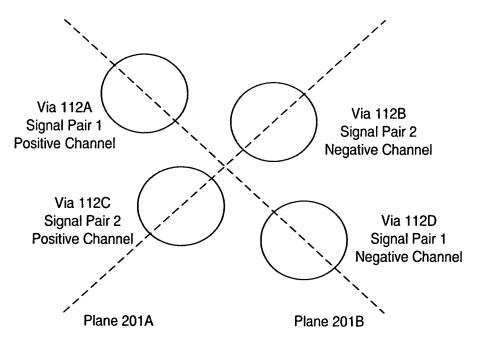


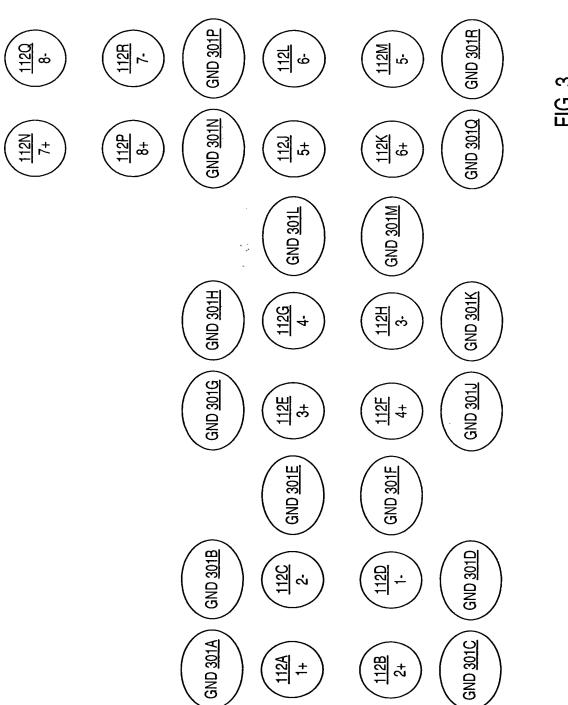
FIG. 2B

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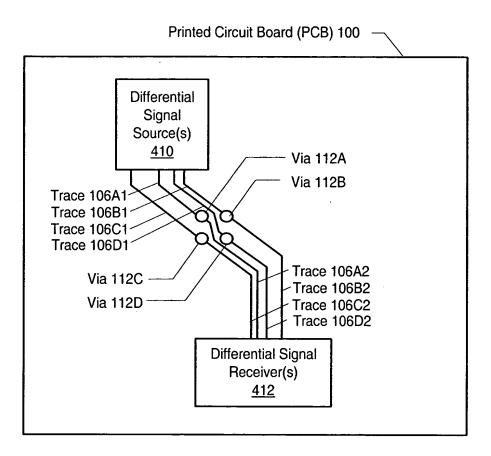
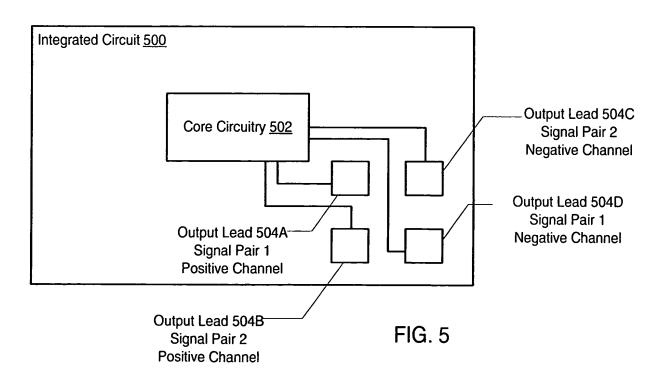


FIG. 4



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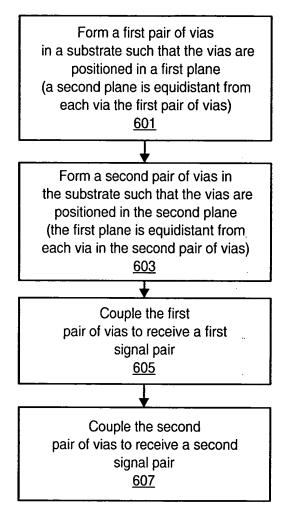


FIG.6